

## Design Criteria for Multistage Microwave Amplifiers with Match Requirements at Input and Output

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An approach to the design of multistage microwave amplifiers in a prescribed frequency band with requirements about the transducer gain flatness and the maximum magnitude of the reflection coefficient at input and output is presented. The interstage equalizers are designed by imposing a suitable constraint on the maximum transducer gain, obtainable directly from the specifications; the input and output equalizer are obtained by imposing only the matching requirement. The method proposed allows a separate design of each network which can be performed either through direct optimization or by means of a numerical synthesis.

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